

Application Number

IDS Flag Clearance for Application

IDS
Information

Content	Mailroom Date	Entry Number	IDS Review	Reviewer
M844	12-03-2001	7	<input checked="" type="checkbox"/>	09-22-2004 08:36:53 kvestal

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L1	1	((detect\$4 or scan or identif\$4) and (divid\$4 or segment) and (real-time or real adj time or real near4 time) and baseline and compar\$4 and record\$4 and histogram and exceed\$3 and surveillance).CLM.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 14:56
L2	5429	((382/103,107,168,169, 171,173281,287) or (348/94,139,152,154,155, 672) or (375/240.18,258, 317) or (358/522) or (708/5,400,820) or (352/39)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/01/11 14:58
L3	3004	2 and (motion or movement\$4 or moving)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:05
L4	2793	3 and (detect\$4 or scan\$4 or identif\$4 or recogni\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:17

L5	2648	4 and (real-time or real aj time or real near4 time)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:09
L6	2336	5 and (compar\$4 or match\$4 or comparison)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:10
L7	2152	6 and (stor\$4 or record\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:17
L8	1503	7 and (segment\$4 or divid\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:13
L9	1347	8 and (computer\$4 or processor or contrOller or CPU or PC)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:18

L10	355	9 and histogram\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:18
L11	20	10 and Hough	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:04
L12	32	(baseline near4 histogram\$4) and segment\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:10
L13	25	12 and (motion or movement\$4 or moving)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:06
L14	25	13 and (compar\$4 or match\$4 or comparison)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:16

L15	25	14 and (real-time or real aj time or real near4 time)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:06
L16	25	15 and (detect\$4 or scan\$4 or identif\$4 or recogni\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:06
L17	8	16 and ((real-time or real aj time or real near4 time) near4 (histogram))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:07
L18	963	4 and (real-time or real near4 time)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:09
L19	881	18 and (compar\$4 or match\$4 or comparison)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:10

L20	1	19 and (baseline near4 histogram\$4) and segment\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:12
L21	2	19 and Hough near4 routine	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:12
L22	8	7 and ((baseline) near4 (segment\$4 or divid\$4))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:15
L23	16	((real-time or real adj time or real near4 time) near4 (segment\$4 or divid\$4)) and ((baseline) near4 (segment\$4 or divid\$4))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:16
L24	16	23 and (compar\$4 or match\$4 or comparison)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:16

L25	16	24 and (detect\$4 or scan\$4 or identif\$4 or recogni\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:17
L26	16	25 and (stor\$4 or record\$4 or memory or database)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:17
L27	15	26 and (computer\$4 or processor or contrOller or CPU or PC)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:18
L28	3	27 and histogram\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/01/11 15:19


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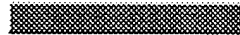
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Christopher, R.J.; Gregson, P.H.;
Electrical and Computer Engineering, 1994. Conference Proceedings. 1994 Canadian Conference
25-28 Sept. 1994 Page(s):576 - 579 vol.2
Digital Object Identifier 10.1109/CCECE.1994.405817

[AbstractPlus](#) | Full Text: [PDF](#)(228 KB) IEEE CNF**2. Combining luminance and edge based metrics for robust temporal video segmentation**

Saez, E.; Benavides, J.L.; Guil, N.;
Image Processing, 2004. ICIP '04. 2004 International Conference on
Volume 4, 24-27 Oct. 2004 Page(s):2231 - 2234 Vol. 4
Digital Object Identifier 10.1109/ICIP.2004.1421541

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 PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = ESTEVEZ

First Name = LEONARDO W.

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09411124	6526430	150	10/04/1999	RECONFIGURABLE SIMD COPROCESSOR ARCHITECTURE FOR SUM OF ABSOLUTE DIFFERENCES AND SYMMETRIC FILTERING (SCALABLEMAC ENGINE FOR IMAGE PROCESSING)	ESTEVEZ, LEONARDO W.
60007377	Not Issued	159	11/20/1995	REAR MOUNTED MOTORIZED ROLLER BLADE	ESTEVEZ, LEONARDO W.
60058880	Not Issued	159	09/15/1997	CAMERA ROTATOR FOR DIGITAL PANORAMIC PHOTOGRAPHY	ESTEVEZ, LEONARDO W.

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="Estevez"/>	<input type="text" value="Leonardo W."/>	<input type="button" value="Search"/>

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